

PathWave Advanced
Design System

PathWave Advanced Design System 2021 Update 0.1 Release Notes

Notices

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ADS 2021 Update 0.1 Release Notes

Release: September 29, 2020

ADS 2021 Update 0.1 (minor update release) is a cumulative minor update release installed on ADS 2021 (base release). You can upgrade your existing ADS installation (ADS 2021) to ADS 2021 Update 0.1 without uninstalling any previous minor updates (if available any).

Version

530.update0.1

Following table lists the application version information:

	<i>Linux</i>	<i>Windows</i>
Design Environment	530.update0.1 Sep 24 2020 (64-bit)	530.update0.1 Sep 25 2020 (64-bit)
Data Display Server	530.update0.1 Sep 24 2020 (64-bit)	530.update0.1 Sep 24 2020 (64-bit)
hpeesofsess	530.shp Aug 11 2020 (64-bit)	530.shp Aug 11 2020 (64-bit)
Momentum MomEngine	2021.01.361 (*) built: Sep 7 2020	2021.01.361 (*) built: Sep 7 2020
FEM engine	390.010 2020-09-15	390.010 2020-09-15
hpeesofsim	530.update0.1 Sep 24 2020, MINT version 5	530.update0.1 Sep 24 2020, MINT version 5
	(64-bit linux built: Thu Sep 24, 2020 03:11:19 +0000)	(64-bit windows built: Thu Sep 24, 2020 16:13:50 +0000)

Platform Support

- Supported Platforms: **Windows and Linux** 64-bit.

Issues Addressed

ADS 2021 Update 0.1 addresses issues related to Circuit Simulation, Data Display, Design and Technology Management, Design Editing, EM Simulation, SIPro/PIPro, and Momentum.

Circuit Simulation

- General
 - Fixed the issue where optimization failed for Angelov model.
 - Transient Simulation now does not result in access violation error.
 - P2D Controller now recognizes sweep plan.
- DDR
 - DDR_PCB now adds resistors to ports when the user selects terminated and specifies a value in the connectivity data table.
 - Fixed the issue where Memory Designer changes the default AMI parameter value.
 - A warning message is prompted when the delay parameter is not a numerical value to prevent unnecessary issues for the simulations.
 - In non-IBIS flow, the DDR_Controller now retains all the signal information when reopened.
 - In non-IBIS flow, the browse and view option for Delay file is always active in the DDR_Memory without activating the option.
 - Fixed the Simulation error - Access violation issue.
 - Memory Designer now supports DBI with DM within single TX.
 - Missing bits between blocks for bit by bit mode simulation for some cases.
 - PCB Custom rule edit now allows user to enter custom separator.
 - Fixed the issue where asynchronous crosstalk waveform create extra points for FlexDCA and Infiniium Probe.
 - Fixed the ADS crash if DDR_Memory is not able to find an *.ebd* file.
 - Fixed the issue where netlist error was prompted while using DDR_PCB.
- SerDes
 - VOL simulation: enforce enabling VPI simulation script.
 - The VPI DS version 11.0 (latest) will throw an error for *.vtmu* file of VPI DS version 10.0 or 10.1 "ADS-VPI DS Python interprocess communication failed error" when performing the simulation. This error is due to registry not being updated during a new VPI DS version installation.
Workaround: The registry should be cleaned by performing the following steps:
 - Open the Windows registry.
 - Remove all the leftover keys for any of the VPI DS versions which are no longer installed.
 - VOL GUI: User-selection of VPI enumeration-type values is not saved.
 - Initial default setting of the IBIS-AMI is now saved by the Rx_AMI.

Data Display

- Fixed issue where equations are not visible on a the page.
- *Draw Outline* check box is unchecked by default in the Edit Text Properties dialog box.
- Fixed the issue where unselected rows were getting saved in Expression Manager.
- Running Swept VTB simulation now combines data into a dataset at the end.

Design and Tech Management

- Fixed Unarchive missing libraries path text getting cut off.
- Fixed Linux possible check workspace hang after renaming library.

Design Editing

- Fixed ADS crash while creating interconnect through AEL functions.

EM Simulation

- FEM
 - Fixed the issue where emSetup based FEM sitecluster distributed job finishes without running.
- RFPro
 - Ability to specify user specified simulation root path.

SIPro/PIPro

- PIPro
 - Import and export target impedance in Decap Optimization window works fine.
 - Generated Sub-circuit now uses the variables defined in the VAR block.
 - Thermal Resistance Extraction Tools works fine when click on it.
 - High-light instance not working in "select actions for connected components" while create ports.
 - Fixed the issue where components placed at the bottom layer are displayed reversed in SIPro geometry window.
 - DC result overview now compares with Electrothermal and thermal results without any errors.
 - Fixed the default "u_str" key issue in the sitecluster Queue due to which submitted commands fails inside the FEM engine.
- SIPro
 - Improved signal matching for DDR Setup and DDR Attribute Editor.
 - Fixed the SIPI view crash when RapidScan-ZO is executed with the hand-made layout attached.

Momentum

- Fixed the "port not fully connected" simulation error message.

