GoldenGate 2020
Update 1.1

# GoldenGate RFIC Simulator 2020 Update 1.1 Release Notes

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### *Released in April 2020*

GoldenGate 2020 Update 1.1 is a minor update release. You can upgrade your existing GoldenGate installation (GoldenGate 2020 Update 1.0) to (GoldenGate 2020 Update 1.1).

### Support discontinued for

- Linux RHEL 5
- Cadence ADE version IC 5.1.41, 6.1.4, and 6.1.5

### Issues Addressed/Enhancements in GoldenGate 2020 Update 1.1

- Allows hisimsoi and hisimsoi_bt in the GG translation file.
- Output currents are now matching ADS for hisimsoi 1.40 model in DC.
- Model improvements: UTSOI Version 2.20, 2.30.  The updated documentation for the models will be available in the next update/major release.
- Fixed the issue in Fast Envelope level 5 where weird results are expected in case the device has strong filtering in-band effects.
- Support for models: HiSIM_SOI Version 1.40, HiSIM_HV Version 2.20, and HiSIM_HV Version 2.40. The updated documentation for the models will be available in the next update/major release.
- Enhancements to the VTB flow:
  - Current sources as modulated inputs to the circuits are now supported in addition to voltage and power sources.
  - Piecewise linear (PWL) sources are now available even in the Fast Envelope flow.
  - Parallel sources with same frequency are now taken into account.

### Compatibility table for GoldenGate in ADS

| GoldenGate Version | ADS Version |
| --- | --- |
| 2020 Update 1.1 | 2020 Update 1.0 and 2020 Update 1.1 |
| 2020 Update 1.0 | 2020 Update 1.0 |
| 2020 | 2020 |
| | 2019 Update 1.0 |

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change without notice.

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