

PathWave  
FPGA 2020

# System Requirements

# Notice

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You must ensure that your system meets the following requirements before installing PathWave FPGA.

- Administrator privileges
- Operating system that has the most recent updates and Service Packs
- License File (or Authorization Codes, or token if evaluating) or internet access

## Recommended Hardware Configurations

Category	Practical Minimums	Recommended
<b>Operating System</b>	Windows 7 SP1, 64-bit (Windows 8 is not supported)	Windows 10, 64-bit
<b>Hard disk</b>	10 GB free space	100 GB free space
<b>RAM</b>	4 GB RAM	16 GB RAM and above
<b>Display</b>	1280 x 720	1920 x 1200
<b>Software Security</b>	USB hardware key	Wired LAN, or Wireless LAN
<b>Test Instrument Interface</b>	Not required	LAN
<b>Touch User Interface</b>	N/A	Not supported

## Software Compatibility with PathWave FPGA

The following table summarizes PathWave FPGA compatibility with various versions of other software applications. However, for the latest vendor information, licensing, and downloads, please contact each vendor directly.

Vendor	Software / Feature	Release Officially Supported	May work, but not supported	Release Explicitly not-supported
<a href="#"><u>Xilinx</u></a>	Vivado, debugging, compilation of bit images.	Vivado 2017.3		prior to Vivado 2017.3
<a href="#"><u>CMake</u></a>	CMake to support to enable FPGA bit file verification	3.9 or later		prior to 3.9
<a href="#"><u>Kactus2</u></a>	To Import HDL with collapsible interfaces using IP-XACT	3.6 or later	3.5 (note, there is a workaround documented when using parameterized HDL)	3.4
<a href="#"><u>Microsoft</u></a>	Visual Studio C++ to enableFPGA bit file verification	2017	Other versions	

## Summary of HDL Language Support

Standard	Release Officially Supported	May work, but not supported	Release Explicitly not-supported
<b>IP-XACT</b>	<a href="#">IEEE 1685-2014</a> , <a href="#">IEEE 1685-2009</a>		
<b>Verilog</b>	<a href="#">IEEE 1364-2005</a>		
<b>VHDL</b>	<a href="#">IEEE 1076-2002</a> (VHDL 2002)		<a href="#">IEEE 1076-2008</a> (VHDL 2008)

Newer versions of Xilinx Vivado might be required for Keysight Instruments (BSPs). Consult the instrument product manual for specific requirements.