

PathWave Advanced
Design System

PathWave Advanced Design System 2021 Update 1.1 Release Notes

Notices

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ADS 2021 Update 1.1 Release Notes

Release: December 21, 2020

ADS 2021 Update 1.1 (minor update release) is a cumulative minor update release installed on ADS 2021 Update 1.0 (base release). You can upgrade your existing ADS installation (ADS 2021 Update 1.0) to ADS 2021 Update 1.1 without uninstalling any previous minor updates (if available).

Version

531.update1.1

Following table lists the application version information:

	<i>Linux</i>	<i>Windows</i>
Design Environment	531.update1.1 Dec 12 2020 (64-bit)	531.update1.1 Dec 11 2020 (64-bit)
Data Display Server	531.update1.1 Dec 12 2020 (64-bit)	531.update1.1 Dec 11 2020 (64-bit)
hpeesofsess	531.update1.0 Nov 1 2020 (64-bit)	531.update1.0 Oct 31 2020 (64-bit)
Momentum MomEngine	2021.10.414 (*) built: Oct 15 2020	2021.10.414 (*) built: Oct 15 2020
FEM engine	391.100 2020-12-09	391.100 2020-12-09
hpeesofsim	531.update1.1 Dec 12 2020, MINT version 5	531.update1.1 Dec 12 2020, MINT version 5
	(64-bit linux built: Fri Dec 11, 2020 19:06:50 +0000)	(64-bit windows built: Sat Dec 12, 2020 05:23:54 +0000)

Platform Support

- Supported Platforms: **Windows and Linux** 64-bit.

Enhancements

ADS 2021 Update 1.1 includes enhancements in Circuit Simulation, Data Display, Design and Technology Management, and RFPro.

Circuit Simulation

- General:
 - Added support for string array MINT model parameter.

Data Display

- Maintain current selection when find and replacing a group of expressions in Expression Manager.
- Improved Expression Manager tabbing.

Design and Technology Management

- Search Utility for Schematic now searches for MeasEqn equations.

RFPro

- RFPro now supports the "-cdslib <path>" Virtuoso argument which forces the cds.lib path.

Issues Addressed

ADS 2021 Update 1.1 addresses issues related to Circuit Simulation, Data Display, Design and Technology Management, Design Editing, EM Simulation, HSD Design, SIPro/PIPro, and Verification.

Circuit Simulation

- General:
 - On Windows, Heap Count information is now removed while simulating.

Data Display

- Fixed Data Display not properly switching from one dataset to another in some situations.
- Fixed potential Data Display crash for large dataset files.
- Fixed crash when hovering over a trace.
- Fixed data indexing for unexpected "invalid" data.
- Fixed AEL import of "workspace_auto_load_files.ael" trying to load itself.

Design and Technology Management

- Fixed on screen editing of parameters for rotated instances.
- Improved Open Workspace navigation for *cds.lib* library definition files.
- Fixed Archive displaying the workspace twice when using a *cds.lib* library definition.
- Fixed the performance issue for large *workspace.ads* file.

Design Editing

- presistor in analogLib now works fine with default Cadence Switch/Stop view list.
- Fixed the Basic Components palette visibility issue.

EM Simulation

- RFPro
 - Under the IC Tools, Cadence Virtuoso and Synopsys Custom Compiler, the Auto mode of Momentum Generation selects Generation 1. You can manually select Generation 2 to try out this new pilot capability.
 - The Python call `empro.toolkit.analysis.loadDesign()` no longer invokes dialogs and can be used in non-interactive scripts.
 - An issue was fixed that could lead to disconnected dot pins in a mixed unit multi-technology design.
 - The near field visualization could hang RFPro in Update 1. This has been addressed.

HSD Design

- DDR:
 - Addressed the issue where deactivated DDR_PCB instance creates problems.
 - Addressed the missing DIMM_Connector file location issue.
 - Addressed the "missing file" netlist error where the EBD file cannot be found.
 - Addressed MD_TLine Copy/paste feature.
 - Fixed the crash issue when the "Show Component Parameter Dialog Box" option is enabled with a MD_Setup or Memory_Probe.
 - Addressed Write leveling delay file bug.
 - Addressed the depreciated DDR_Package issue with new controller and memory components.
 - Enhanced a missing IBIS file behavior: When an .ibs file is not found for the DDR_Controller or DDR_Memory, a prompt appears to browse the file.
 - Fixed the netlist error caused by a non target model with non-IBIS flow.
 - Fixed DDR_PCB Check/View S-Parameters options.
 - Addressed unsupported measurements for DQS and CK signals (for example, Mask Margin measurement is unsupported) in Memory Probe.
 - The warning message no longer appears for the IBIS_Pin component (differential, old, and unbalanced IBIS_Pin) when opened again.
 - Fixed the netlist file error, when an IBIS file is defined as a variable or called through a VAR or NetlistInclude component.
 - DDR_Controller/DDR_Memory: Fixed the issue, when a workspace was saved with a relative path and cannot be not opened the next time.
 - DDR_Controller/DDR_Memory: Performance improved when you click the OK/Apply button.

- SerDes:
 - PAM4 symbol sequence with VtPRBS is now translated correctly to bit sequence.
 - When PAM4 symbol file is used in TX, FlexDCA now includes the correct pattern length of the output waveform file generated.
 - Fixed the issue, where simulation was terminated due to error when using Smart_Eye_Probe with Eye_Probe.
 - Waveform generated by Smart_Eye_Probe and EyeDiff_Probe now show same results.

SIPro/PIPro

- The near field visualization could hang PIPro in Update 1. This has been addressed.
- PI-AC analysis with reference pins now does not prompts error that pins found at layers not present in meshed design.
- Fixed the simulation results where "DC/ETH" has reference pins on the layer.
- Fixed generation of stimulus file in case of empty component model.
- Added the ODB++ exporter for ADS. It is a beta release.
- Improved the warning message where Decap optimizer was not opening or initializing for analysis copied from SI analysis created with the DDR Setup tool.
- Fixed the sipiSetup crash while switching component model type from Lumped to ModelDB in multiple component model editor window when PDN impedance plot is active in background
- Fixed the sipiSetup crash when you try to open PDN impedance and then open Decap Optimization.
- Added a warning message to handle a situation where a reference pin on layer and a plus pin are on the same layer.
- Fixed the issue where deprecation warnings were listed while importing/exporting the thermal models in ETH analysis.

Verification

- Importing ADS netlist file now does not prompt an Encode design error.

